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Proteus ISIS simulation for power factor calculation using zero crossing detector

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Abstract

One of the important parameters for electrical systems is the power factor (cos phi), which is the ratio of the real power (watt) to the apparent power (volt ampere). The best cos phi value is between 0.85 to 1. A resistive load causes the voltage and current in equal phase angle, while the inductive load causes the current to lag behind the voltage. On the other hand, the capacitive load causes the current to precede the voltage (leading). A simulation to determine the power factor of an electrical network can be done with Proteus ISIS software by creating a phase detection circuit. Automatic control can be done by a microcontroller. This simulation circuit can be used as power factor correction, a trigger angle on SCR trigger for DC motor speed control, for rocket launch angle adjuster, to measure the angle of inclination, and other uses relating to angle adjustments.

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Keywords: power factor; cos phi; zero-crossing.

I. Introduction

Power quality determines the reliability of the electrical system. One of the important parameters to be considered for electrical system reliability is a power factor (cos phi). Power factor is determined by how far the phase degree difference (cos) between voltage and current. Understanding the power factor and how it affects the utility bill can help reduce electricity costs. The effect of load on generator performance, particularly output power, efficiency, and voltage regulation, can be seen by designing two types of simulated loads: pure resistive and series resistive-inductive loads. Each type of load provides a power factor of 1 and 0.85, respectively. The simulation results show that the generator gives better performance when loaded with resistive loads [1]. The SEPIC converter works in intermittent conduction mode and functions as a power factor corrector so that the input current waveform follows the input voltage waveform. The full-bridge DC-DC converter functions as an output

* Corresponding Author. Phone: +62-813-4592-0696 *E-mail address*: jumrianto@ivet.ac.id voltage regulator and operates in a continuous conduction mode, where the power factor of this converter system can achieve up to 0.96 [2]. If the current and voltage are both sinusoidal in one phase, the power factor is 1.0. If both are sinusoidal but in phase difference, the power factor is the cosine of the phase angle [3]. Zero-crossing Detector functions as a triggering start for the PWM signal so that the fan speed and light brightness can be adjusted [4]. Zero-crossing detectors are used to synchronize switching with the AC wavelength or to extract the timing signal. By using zero-crossing techniques, high inrush currents can be avoided [5]. The Zerocrossing Detector circuit functions well as a voltage safety net for the Indonesian State Electricity Company [6]. Different approaches are used for zerocrossing: hardware or software. The hardware approach is simple, but it has low precision. On the other hand, the software approach has high precision, but it is complicated and expensive [7]. Zero-voltage cross detection is commonly used as an accurate method of detecting AC characteristics, such as frequency and phase. The zero-crossing point is ideally suited for measuring AC signals for several reasons. A comparator is similar to an operational amplifier (Op-Amp). It has two inputs,

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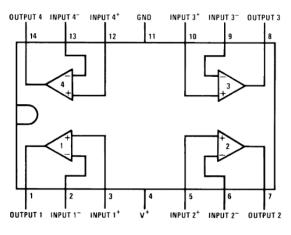


Figure 1. Schematic diagram of IC LM-324

inverting and non-inverting, and an output, but it is specifically designed to compare the voltages between its two inputs. A suitable zero-crossing circuit is often needed for devices such as lighting controllers, thermostats, motor drives, and similar AC-load controlling or monitoring applications, often needs mains phase and/or frequency information [8].

II. Materials and Methods

The phase difference is obtained by comparing the phase voltage and phase currents drawn by the load. The voltage and current waves of a sensor are still sine signals where the two phases are different [9]. A comparator is used to distinguish between two different signal levels [10].

Comparator is similar to Op-Amp. A comparator has two inputs (inverting/non-inverting) and output. A comparator is a digital circuit in which all the output values are expressed as "yes = 1" or "no = 0". To facilitate the processing of a sine wave, a comparator is used to convert a sine wave into a square wave. Among the ICs that can be used is IC LM324, which has a 5 volt DC power supply source.

A. Comparator

The phase difference detector is made using Comparator and Exclusive-OR logic gates. The comparator is used to convert a sine wave into a square wave, while the Exclusive-OR logic gate is used to pass a square wave (value 1) only when the two inputs are different. If the input waveform is equal, either equals 0 or equals 1, then the output will be 0.

The length of time the output is 0 or 1 depends on the magnitude of the phase angle. The cos difference value is obtained by calculating the time interval between the rising voltage and the down voltage at the Exclusive-OR logic gate output [11]. Design of a comparator block using one of the facilities provided by operational amplifier (Op-Amp) type LM-324 [9]. The schematic image of IC LM-324 [12] used as a comparator is shown in Figure 1.

B. Exclusive-OR logic gates (Ex-Or)

The Exclusive-OR logic gate used is IC 74LS86. The term Exclusive-OR is abbreviated as Ex-OR. The

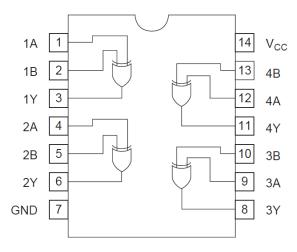


Figure 2. Schematic diagram of IC 74LS86

IC used as a detector of phase difference output is IC 74LS86, as shown in Figure 2 [13]. The Exclusive-OR gate will only open when an odd number unit appears in the input [14].

C. Power factor

Power is measured in watts. Watt is the voltage multiplied by the current multiplied by the power factor. Watts are just the same as the volts multiplied by amperes when the power factor is 1, but most of the power factor is less than 1 [15]. The power factor value depends on the phase difference between the current and voltage [11]. When electricity is applied to a pure inductor, no real work is done, and no heat or light is produced.

The reactive power is measured in kilo-voltampere-reactive (kVAR). Examples of inductive loads are transformers, motors, and lighting ballast. The current through one of these R, L, and C components will have the same AC frequency as the voltage source that drives the current. In other words, there exists a phase shift between current and voltage. The resistive element is the only component that produces clean energy. The actual power consumed by a circuit is real power in watts, the voltage and current might not be in one phase.

Figure 3 forms a power triangle where the apparent side is rms volt multiplied by rms ampere. This is called the apparent power (S) and has a unit of volt-amp (VA). The real power is expressed by $P = VI \cos \theta$, which is the real power in watts. The vertical side of the triangle, $Q = VI \sin \theta$, is called reactive power and has a volt-amps-reactive VAR unit. Reactive VAR power is not capable of doing any work [16]. Figure 3 shows the power triangle [17].

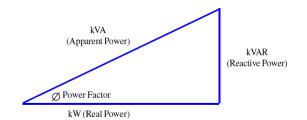


Figure 3. The power triangle displays the apparent power S (voltamp) converted to reactive power (VAR) and real power P (watts)

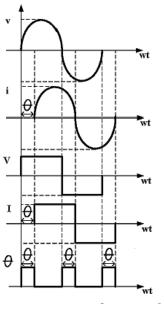


Figure 4. Sinus waves, square wave, and cos in (t)

D. Zero-rrossing detector

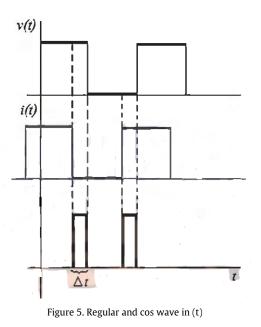
In Zero cross detection method for the time synchronization purpose is based on phase locked loop. Simulation schema is introduced to examine and analyze PLL behavior under different grid interferences. Two potential issues multiple zerocrossing in the zero cross-area may cause instability of the loop and the presence of harmonics may cause shifts of the zero-cross event [18]. A zero-crossing detector circuit is an important application of an Op-Amp comparator circuit. It can also be referred to as a sine to a square wave converter. All inverting or non-inverting comparators can be used as zerocrossing detectors. The input sine wave is given as Vin [19]. Comparators are used to distinguish between two different signal levels.

For example, a comparator can distinguish between excessive temperature and normal temperature conditions [10]. Zero voltage detection is generally used as an accurate method for detecting AC characteristics, such as frequency and phase. The ideal zero-cross point for measuring signal [20]. A zero-crossing detector can have two types of output signals. The zero-crossing should be during the transition between positive and negative input signals [7]. Zero-crossing is the point of choice for measuring phase and frequency. References are usually easy to establish and the rate of change in maximum signal amplitude at zero signal [21]. The input and output wave graphs in the zero-crossing circuit of the phase difference detector can be seen in Figure 4.

From Figure 5 it is stated that θ is the phase angle (cos) sought after being converted to an angle in degrees. Phi (ϕ) can be calculated by the following formula:

$$\phi = \Delta t / T \times 3600 [11]$$
 (1)

 ΔT the square wave in Figure 5 is the cos value in time, since cos is in proportion to cos in degrees and is inversely proportional to the value of cos phi.



In Figure 5, v(t) and i(t) is a square wave for input and Δt is a square wave for output. From Figure 5, cos can be calculated by the following formula:

$$\phi = \Delta t / 0.5T \times 1800 [9] \tag{2}$$

where: ϕ = phase difference in degrees (°), Δt = phase difference in time (ms), t 1/2 T = half time period signal (ms). Cosine value of the phase difference in angle is the value of power factor sought.

E. ISIS proteus simulation software

ISIS stands for Intelligent Schematic Input System and is one of the simulation programs that is integrated with Proteus and has become its main program. ISIS was designed as a medium for drawing electronic circuit schematics in accordance with international standards. With PCB layouts now offering component placement automation and routing paths, getting the design into a computer can often be the most time-consuming element of practice.

F. Simulation set-up

The main circuit design of the power factor calculation is shown in Figure 6.

1) Design a differential phase detector (zerocrossing detector)

The phase difference between the voltage and current can be detected by a zero-crossing detector. The zero-crossing detector phase difference circuit is shown in Figure 7.

Voltage and current waves at voltage sensor and the current sensor through the comparator, the output of the LM 324 IC will be fed to an Exclusive-OR logic gate that can detect the time rising edge and time falling edge at the voltage and current waves.

2) Set the input current phase

As a simulation of phase change between the input current voltage, cos is done in degrees as the angle varies.

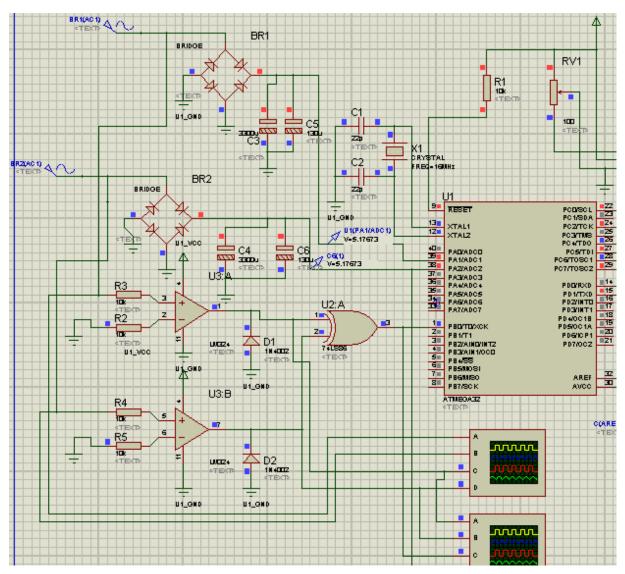


Figure 6. Main circuit design connected to a microcontroller

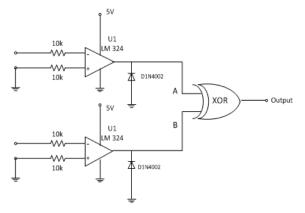


Figure 7. Draw zero-crossing detector circuit

3) Running the simulation

The voltage and current waves are displayed by starting the simulation and pressing the play button on the left corner of the computer or laptop.

4) Displays digital oscilloscope

The oscilloscope can be seen by right-clicking on the digital oscilloscope. A square wave of inputs and outputs in the oscilloscope will appear. After the wave appears, point the cursor at the start of the ascending wave and the end of the wave down on the positive side. Phi value in time is 9.9 ms - 5.9 ms = 4 ms.

5) Convert the phi value to cos phi

The value of Δt is converted to phi by the formula: $\phi = (\Delta t / 0.5T) \times 180$ or $\cos = (\Delta t / T) \times 360$, then by calculating the value of phi, we get the value of power factor (cos phi).

III. Results and Discussions

The test results show that there is a change in phase angle when the phase of the input current is increased or decreased. The phase angle change value is shown in Table 1.

The value of cos phi recommended by the Indonesian Electrical Power Company is 0.85 to 1, then the analysis of cos phi starts from the value of 31.5° to 0°. From Table 1, it can be seen that by changing the phase angle of the input current in degrees, we can calculate the value of cos phi with the following analysis:

Table 1. Comparison of phase angle (cos) in time, in angle (°) and cos phi

No	Angle (degree)	Time (millisecond)	cos phi
1	0	0	1
2	4.5	0.25	0.997
3	9	0.5	0.987
4	13.5	0.75	0.972
5	18	1	0.951
6	22.5	1.25	0.923
7	27	1.5	0.891
8	31.5	1.75	0.852

A. Phase current analysis over phase voltage

1) Phase current precedes phase voltage at 31.5°

The phase current precedes the phase voltage of 31.5°. The current and voltage waves can be seen in Figure 8. The incoming and outgoing square wave of IC 74LS74 with a phase angle of 31.5° is shown in Figure 9. The output wave IC of IC 74LS86 is an indicator of the enlarged phase difference to find the

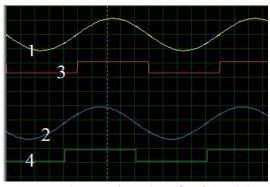


Figure 8. Current phase precedes a voltage of 31.5°: (1 and 3): Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave output of IC LM-324 to IC 74LS86

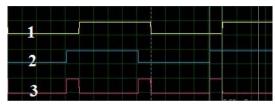


Figure 9. Wave input and output IC 74LS86 with the phase angle 31.5°; (1) Current-phase currents; (2) Square wave phase voltage; (3) Square wave difference of phase current and voltage

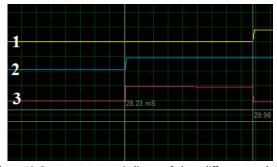


Figure 10. Square wave as an indicator of phase difference at phase difference 31.5°: (1) Current phase input waves to IC 74LS86; (2) Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

time duration of the voltage rises until the voltage drops, which is used as a phase or cos phase in time. Cos in time is converted to degrees before converted into cos phi to be known. Figure 10 shows the phase difference in units of time.

When the phase difference is 31.5°, the value of Δt can be determined by measuring the output pulse IC 74LS74, when the pulse is up at 8.20 ms. When the pulse drops at 9.90 ms, then Δt is $\Delta t = 29.98-28.23 = 1.75$ ms. Therefore, the value of phi is $\phi = (1.75 / 10) \times 180$; $\phi = 0.175 \times 180$; $\phi = 31.5^{\circ}$; Then the value of cos phi is cos phi = cos 31.5° = 0.852.

2) Phase current precedes phase voltage at 27°

When the phase current precedes the phase voltage of 27°, the current and voltage waves can be seen in Figure 11. The incoming and outgoing square wave of IC 74LS74 at 27° phase is shown in Figure 12.

Cos in time is converted to degrees and then converted into cos phi to be known, as shown in Figure 13. When the phase difference is 27° , the value of Δt can be determined by measuring the

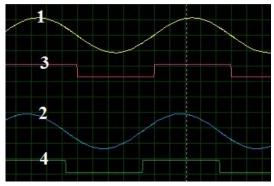


Figure 11. Current phase precedes a voltage of 27°. (1 and 3) Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave output of IC LM-324 to IC 74LS86



Figure 12. Wave input and output IC 74LS86 at phase angle 27°. (1) Current-phase currents (2) Square wave phase voltage; (3) Square wave difference of phase current and voltage

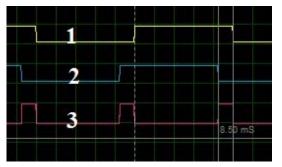


Figure 13. Square wave as an indicator of phase difference at moment 27°. (1) Current phase input waves to IC 74LS86; (2) Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

output pulse IC 74LS74. When the pulse rises at 8.5 ms and drops at 10 ms, then Δt is $\Delta t = 10 - 8.5 = 1.5$ ms. Therefore, the value of phi is $\phi = (1.5 / 10) \times 180$; $\phi = 0.15 \times 180$; $\phi = 27^{\circ}$. Then the value of cos phi is cos phi = cos $27^{\circ} = 0.891$.

3) Phase current over phase voltage at 22.5°

When the phase current precedes the phase voltage of 22.5°, the current and voltage waves can be seen in Figure 14. The incoming and outgoing square wave of IC 74LS74 when the phase angle is 22.5° is shown in Figure 15.

Cos in time is converted to degrees before converted to cos phi, which is shown in Figure 16. When the phase difference is 22.5°, the value of Δt can be determined by measuring the output pulse IC 74LS74. When the pulse rises at 8.75 ms and drops at 10 ms, then Δt is $\Delta t = 10 - 8.75 = 1.25$ ms.

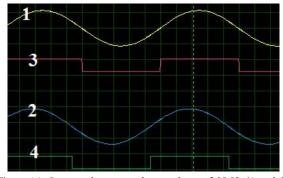


Figure 14. Current phase precedes a voltage of 22.5°. (1 and 3) Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave output of IC LM-324 to IC 74LS86

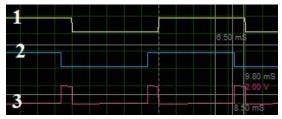


Figure 15. Wave input and output IC 74LS86 at phase angle 22.5°. (1) Current-phase currents; (2) Square wave phase voltage; (3) Square wave difference of phase current and voltage

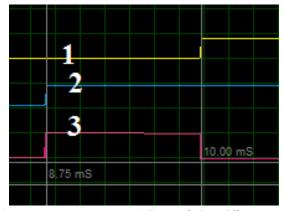


Figure 16. Square wave as an indicator of phase difference at a different phase of 22.5°. (1) Current phase input waves to IC 74LS86; (2) Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

Therefore, the value of phi is $\phi = (1.25 / 10) \times 180$; $\phi = 0.125 \times 180$; $\phi = 22.5^{\circ}$, then the value of cos phi is cos phi = cos 22.5° = 0.923.

4) Phase current over phase voltage at 18°

When phase current precedes a phase voltage of 18°, the current and voltage waves can be seen in Figure 17. A square wave is in and out of IC 74LS74 at the phase angle of 18°, as shown in Figure 18.

Cos in time is converted to degrees and converted into cos phi to be known, as shown in Figure 19. When the phase difference is 18°, the value of Δt can be determined by measuring the output pulse IC 74LS74. When the pulse is up at 8.9 ms and drops at 9.9 ms, the Δt is $\Delta t = 9.9 - 8.9 = 1$ ms. Therefore, the value of phi is $\phi = (1/10) \times 180$; $\phi = 0.1 \times 180$; $\phi = 18^\circ$. The value of cos phi is cos phi = cos $18^\circ = 0.951$.

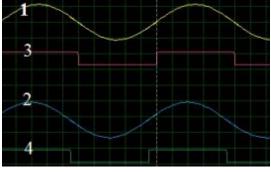


Figure 17. Current phase precedes a voltage of 18°. (1 and 3) Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave output of IC LM-324 to IC 74LS86

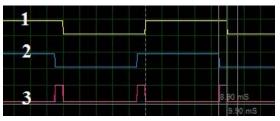


Figure 18. Wave input and output IC 74LS86 at phase angle 18°. (1) Current-phase currents; (2) Square wave phase voltage; (3) Square wave difference of phase current and voltage

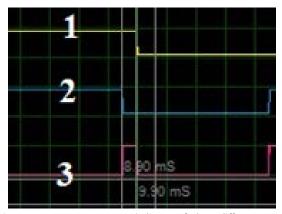


Figure 19. Square wave as an indicator of phase difference at a different phase of 18°. (1) Current phase input waves to IC 74LS86; (2) Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

5) Phase current over phase voltage at 13.5°

When the phase current precedes a phase voltage of 13.5°, the current and voltage waves can be seen in Figure 20. A square wave is in and out of IC 74LS74 at phase angle 13.5°, as shown in Figure 21.

Cos in time is converted to degrees before converted to cos phi as shown in Figure 22. When the phase difference is 13.5°, the value of Δt can be determined by measuring the output pulse IC 74LS74. When the pulse rises at 9.25 ms and drops at 10 ms, the Δt is $\Delta t = 10 - 9.25 = 0.75$ ms. Therefore, the value of phi is $\phi = (0.75/10) \times 180$; $\phi =$ 0.075 x 180; $\phi = 13.5^\circ$. The value of cos phi is cos phi = cos 13.5° = 0.972

6) Phase current over phase voltage at 9°

When the phase current precedes a phase voltage of 9°, the current and voltage waves can be seen in Figure 23. Square wave is in and out of IC 74LS74 at phase angle 9° as shown in Figure 24.



Figure 20. Current phase precedes a voltage of 13.5°. (1 and 3) Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave output of IC LM-324 to IC 74LS86



Figure 21. Wave input and wave output IC 74LS86 at phase angle 13.5°. (1) Current-phase currents; (2) square wave phase voltage; (3) Square wave difference of phase current and voltage

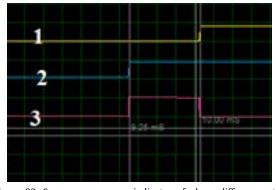


Figure 22. Square wave as an indicator of phase difference at a different phase of 13.5°. (1) Current phase input waves to IC 74LS86; (2) Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

Cos in time is converted to degrees before converted into cos phi to be known, as shown in Figure 25. When the phase difference is 9°, the value of Δt can be determined by measuring the output pulse IC 74LS74. When the pulse rises at 9.25 ms drops at 10 ms, the Δt is $\Delta t = 10 - 9.5 = 0.5$ ms. Therefore, the value of phi is $\phi = (0.5/10) \times 180$; $\phi = 0.05 \times 180$; $\phi = 9^{\circ}$. The value of cos phi is cos phi = cos 9° = 0.987.

7) Phase current over phase voltage at 4.5°

When phase current precedes a phase voltage of 4.5°, the current and voltage waves can be seen in Figure 26. Square wave is in and out of IC 74LS74 at phase angle of 4.5° as shown in Figure 27.

Cos in time is converted to degrees before converted to cos phi, as shown in Figure 28. When the phase difference is 4.5° , the value of Δt can be determined by measuring the output pulse IC 74LS74. When the pulse is up at 39.75 ms and down

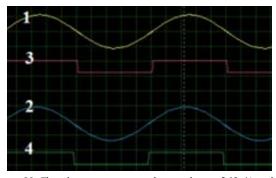


Figure 23. The phase current precedes a voltage of 9°. (1 and 3) Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave of LM-324 IC output to IC 74LS86

1		
-2		10.00 =5
3	<u></u>	9.50 mS

Figure 24. Wave input and output IC 74LS86 at phase angle 9°. (1) Current-phase currents; (2) Square wave phase voltage; (3) Square wave difference of phase current and voltage

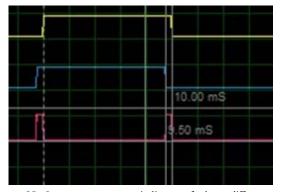


Figure 25. Square wave as an indicator of phase difference at different phase 9°. (1) Current phase input waves to IC 74LS86; (2)°Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

is at 40 ms, the Δt is $\Delta t = 40 - 39.75 = 0.25$ ms. Therefore, the value of phi is $\phi = (0.25 / 10) \times 180$; $\phi = 0.025 \times 180$; $\phi = 4.5^{\circ}$. The value of cos phi is cos phi = cos $4.5^{\circ} = 0.996$.

8) Phase current over phase voltage at 0°

When phase current precedes a phase voltage of 0°, the current and voltage waves can be seen in Figure 29. Square wave is in and out of IC 74LS74 at phase angle 0° as shown in Figure 30.

Figure 31 shows a wave input and output IC 74LS86 at phase angle 0°. When the phase difference is 0°, the value of Δt can be determined by measuring the output pulse IC 74LS74. When the pulse is up at 9.9 ms and drops at 9.9 ms, then Δt is $\Delta t = 9.9 - 9.9 = 0$ ms. Therefore, the value of phi is $\phi = (0/10) \times 180$; $\phi = 0 \times 180$; $\phi = 0^{\circ}$. The value of cos phi is cos phi = cos 0° = 1. Figure 32 shows the circuit and simulation of cos phi = 1.

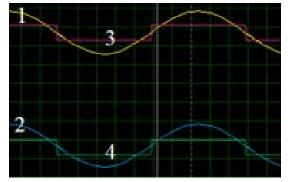


Figure 26. Current phase precedes a voltage of 4.5°. (1 and 3) Wave input current from source; (2 and 4): Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave of LM-324 IC output to IC 74LS86



Figure 27. Wave input and output IC 74LS86 at phase angle 4.5°. (1)° Current-phase currents; (2) Square wave phase voltage; (3)°Square wave difference of phase current and voltage

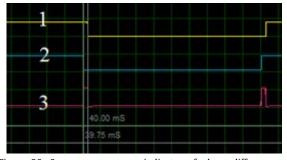


Figure 28. Square wave as an indicator of phase difference at different phase 4.5°. (1) Current phase input waves to IC 74LS86; (2) Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

B. The Correlation between phase shift, cos angle, and cos phi

The correlation between the cos angle (degree) and cos phi can be seen in Figure 33. From Figure 33, it is found that the correlation between the phase difference in degrees and cos phi is inversely proportional; the greater the phase difference, the smaller the cos phi. This has a bad effect on the quality of the electrical power obtained by the customer. The correlation between the phase shift (time) and cos phi can be seen in Figure 34.

From Figure 34, it is found that the correlation between the phase difference in time and cos phi is inversely proportional; the greater the phase difference the smaller the cos phi. This means that the greater the difference phase in time, the worse the quality of the electric power produced.

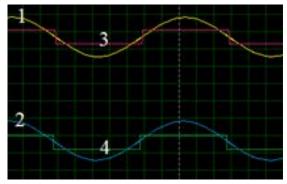


Figure 29. Current phase precedes a voltage of 0° . (1 and 3) Wave input current from source; (2 and 4) Wave input voltage from source; (1 and 2) Sine wave input current and voltage to IC LM-324; (3 and 4) Square wave output of IC LM-324 to IC 74LS86

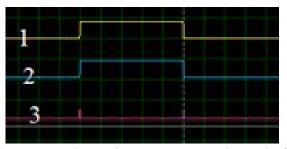


Figure 30. Wave input and output IC 74LS86 at phase angle 0°. (1)°Current-phase currents; (2) Square wave phase voltage; (3)°Square wave difference of phase current and voltage

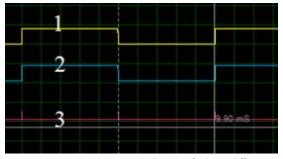


Figure 31. Square wave as an indicator of phase difference at different phase 0° . (1) Current phase input waves to IC 74LS86; (2)°Wave input of phase voltage to IC 74LS86; (3) Wave output of IC 74LS86 as phase angle in time

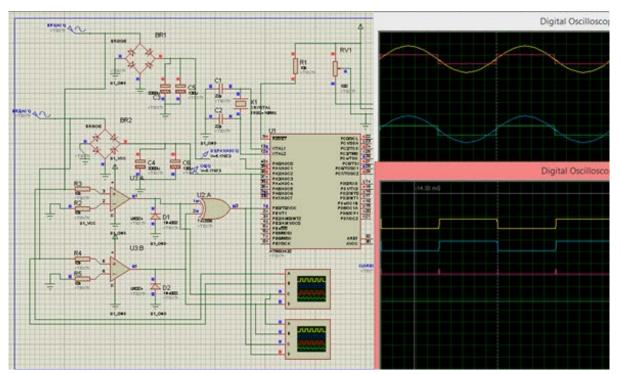


Figure 32. Circuit and simulation of the value cos phi = 1

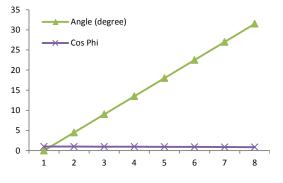


Figure 33. Correlation between the cos angle (°) and cos phi

IV. Conclusion

The amount of power factor or cos phi depends on the difference between the wavelength of the current wave to the voltage wave. The phase difference in time and angle is inversely proportional to cos phi. The longer the lag time between voltage wave and current wave, the smaller the cos phi. This, of course, because phi gets smaller away from 1. If cos phi is smaller than 1, then the power quality on the network is worse. This will certainly increase the costs to be paid on the customer side because it does not match the costs paid to the power used. For electric power providers, this will bring disadvantages, as the efficiency level of the generating power device will decrease. The novelty of this simulation circuit can be used as a trigger angle on SCR trigger for DC motor speed control, for rocket launch angle adjuster, to measure the angle of inclination and other uses relating to angle adjustments. By connecting it to a microcontroller, this circuit can be used to perform various control switching, speed control, precision angle control, and others.

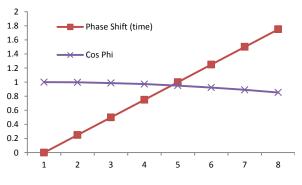


Figure 34. Correlation between the phase shift (time) and cos phi

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Declarations

Author contribution

All authors contributed equally as the main contributor of this paper. All authors read and approved the final paper.

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Conflict of interest

The authors declare no known conflict of financial interest or personal relationships that could

have appeared to influence the work reported in this paper.

Additional information

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